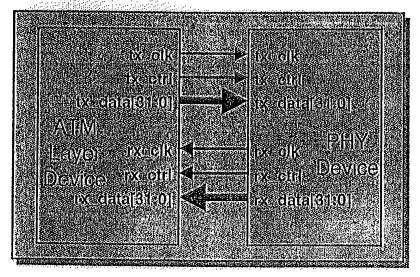
Fig. 1



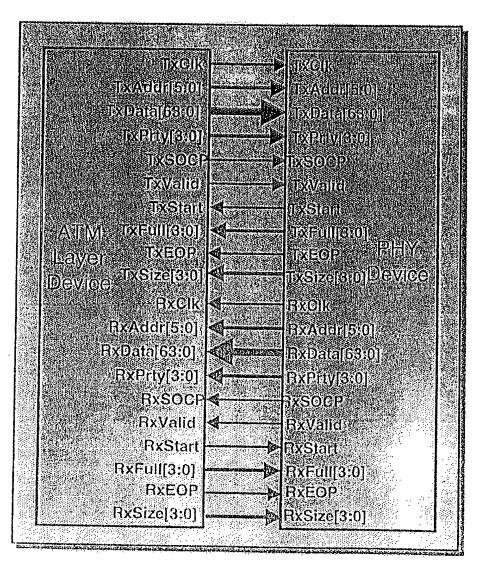
PRIOR ART

Fig. 2

		<b>SUBMITTE</b>	li Format il	
		The state of the s		
131130	20	28; 27:26	25:24 23:8	7:0
30011	Soci	28 EOP	25:24 reserved,	adi[7.0]
16 (4.0)		<u> </u> 27;26 - 4	- 23:16 cong[7:0],	
	*****	MOD	[ 15:8 rel[7:0]	
01**	# t. 00	, DIKI	3:0]   E   Full[2	(3:0]
	(01:1	7	reserved	
1053	insi e in	0000	reserved	[prty[7.0]
	00	01-1111	reserved	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
11		0000	reserved	HEC

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Fig. 3



PRIOR ART.

Fig. 4.

Flow Control Example for 16 PHY Ports	
CIKING	4
+ 2.3 full =+ 8.0 tull = + 1.0 tull =	
* 2,3 full * * * 8,9 full + * * 10 1 full * 5,6 full + * 5,5 full * * 5,6 full * 5,6 f	
Füll[0][Z:PHY-0], PHY-4/PHY-8 PHY-12/PHY-0 PHY-4/	
Enl[1] DHAN BHA? BHA.9/ BHANA BHAN ZABAA.	
Full[2][7:PHY:23:PHY:6\PHY:10;PHY:14;*PHY:2\7:PHY:6\7	
Full[3][\\PHY3/\PHY7\PHY11\PHY45\\PHY4;\PHY4;\	
Manual Commission of the Commi	

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Fig. 5

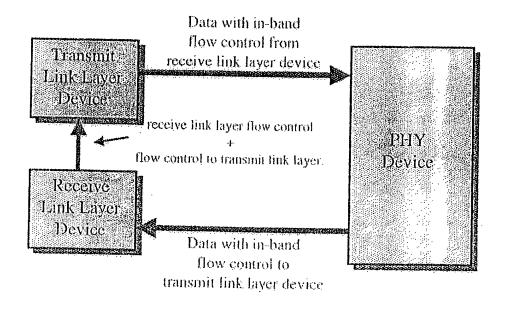
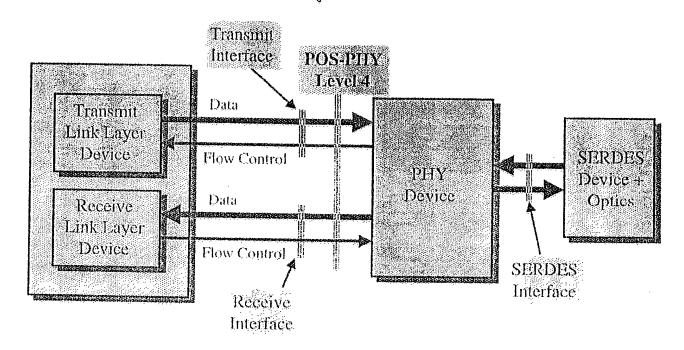


Fig. 6



F16.7

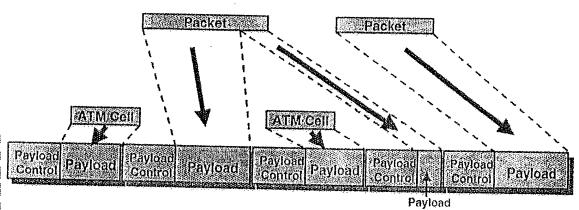


FIG. 8

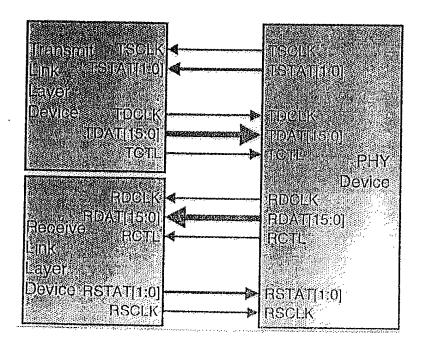


FIG. 9

E	Bit 15 Bit 8	Bit 7 . Bit 0
Data Word 1	Byte 1	Byte 2
Data Word 2	Byte 3	Byte <sub>'</sub> 4
Data-Word 3	Byte 5	Byte 6
Data Word 4	Byte 7	Byte 8
Data Word 21	Byte 41	Byte 42
Data Word 22	Byte 43	XX'

FIG. 10

	Bit 15 Bit P	Bit 7 Bit 0
Data Word	li Hi	
Date Word	H3	FIA .
<ul> <li>Data-Word 8</li> </ul>		ïP2
Dria Woją 2	P8 ;	P4
<ul> <li>Data Word(25)</li> </ul>		P46
- Data Word 26	*** P47	P¥8

FIG. 11

В		8 Bit 7 Bit 0
Data Word 1 Data Word 2	H1 H3	H2 H4
Data Word 3	HEC	UDF
Data Word 4 Data Word 5	P1	P2
walawoju o	P8	P4
Data Word 26	P45	P46
Data Word 27	P47	P48

FIG. 12

J. H	it 15: Bit 8	Bit 7 Bit 0
Data Words		d'Address
Data Word 2	Bÿte 1	Byte 2
Data Word 8	*Byte:0	Eyle 4
- LData Word 4	Byte 5	Byte 6

FIG. 13

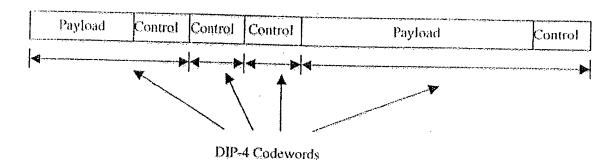


FIG. 14

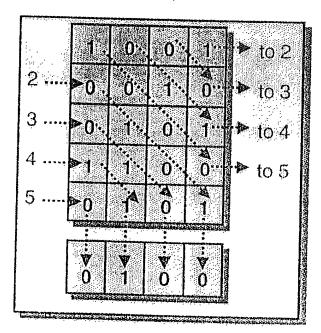


FIG. 15

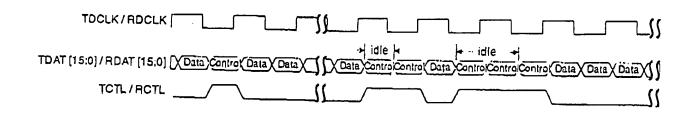


FIG. 16

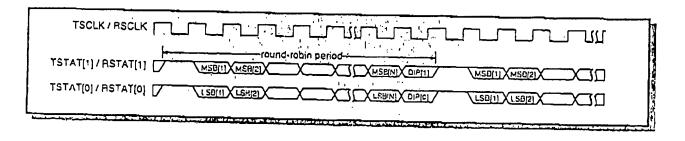


Fig. 17

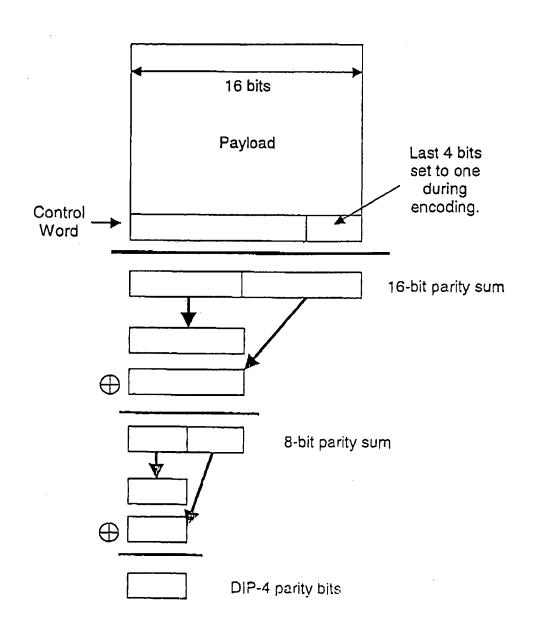
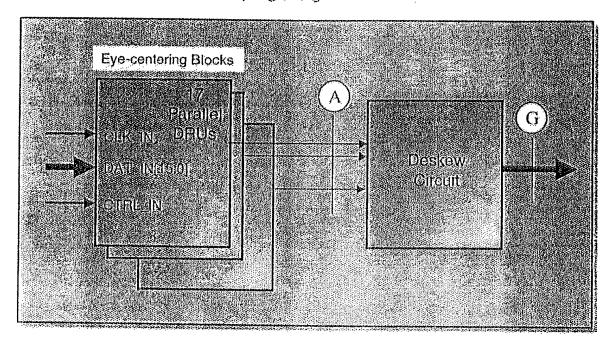


FIG: 18



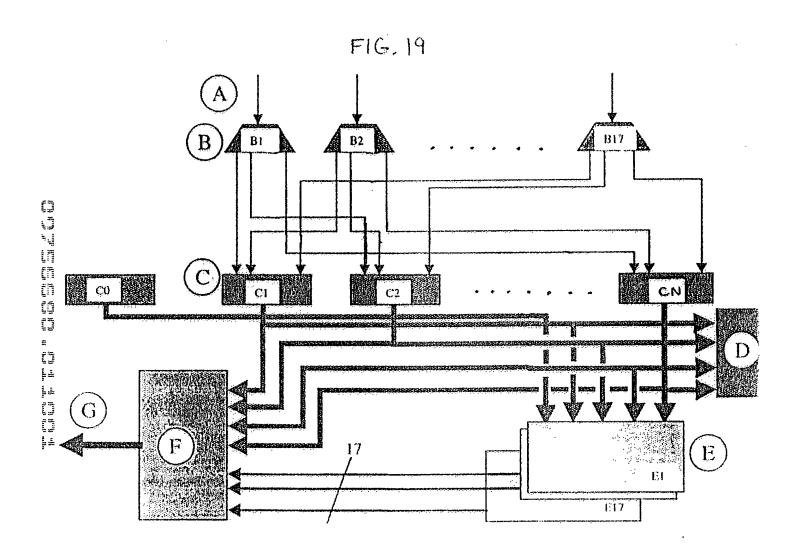


FIG. 20

